COURSE INFORMATON					
Course Title	*Code*	*Semester*	*C +P + L Hour*	*Credits*	*ECTS*
MS thesis	EE600	Fall/Spring	0+0+0	0	60

Prerequisites	

Language of Instruction	English
Course Level	Master's
Course Type	Core
Course Coordinator	Prof. Dr. Duygun Erol Barkana
Instructors	All faculty members
Assistants	
Goals	To finish MS thesis.
Content	Specific for each MS thesis.

Learning Outcomes	**Program Outcomes**	**Teaching Methods**	**Assessment Methods**
1) To be able to make scientific research	1,2,3,4,5,6,7,8,9, 10, 11, 12	2,3	E
2) To be able to make experiments related to thesis.	1,2,3,4,5,6,7,8,9, 10, 11, 12	2,3	E
3) To be able to evaluate the results.	1,2,3,4,5,6,7,8,9, 10, 11, 12	2	E
4) To be able to develop new methods.	1,2,3,4,5,6,7,8,9, 10, 11, 12	2,3	E
5) To be able to report and present new results.	1,2,3,4,5,6,7,8,9, 10, 11, 12	4	E

Teaching Methods:	1: Lecture, 2: Problem Solving, 3: Simulation, 4: Seminar, 5: Laboratory, 6: Term Research Paper
Assessment Methods:	A: Exam, B: Quiz, C: Experiment, D: Homework, E: Project

COURSE CONTENT

Week	Topics	Study Materials
1-14	Specific for each MS thesis work	

RECOMMENDED SOURCES	
Textbook	Scientific papers
Additional Resources	

MATERIAL SHARING	
Documents	
Assignments	
Exams	

ASSESSMENT		
IN-TERM STUDIES	**NUMBER**	**PERCENTAGE**
Thesis study	1	100
Total		**100**
CONTRIBUTION OF FINAL EXAMINATION TO OVERALL GRADE		0
CONTRIBUTION OF IN-TERM STUDIES TO OVERALL GRADE		100
Total		**100**

COURSE CATEGORY	Core Course

COURSE'S CONTRIBUTION TO PROGRAM							
No	Program Learning Outcomes	Contribution				
		1	2	3	4	5
1	Can reach information in breadth and depth, and can evaluate, interpret and apply this information to scientific research in the area of Electrical and Electronics Engineering.					X
2	Can complete and apply information with scientific methods using limited or missing data; can integrate information from different disciplines.					X
3	Sets up Electrical and Electronics Engineering problems, develops and implements innovative methods for their solutions.					X

4	Develops new and/or original ideas and methods; finds innovative solutions to the system, component, or process design.					X
5	Has comprehensive knowledge about the state-of-the-art techniques and methods in Electrical and Electronics Engineering and their limitations.					X
6	Can design and conduct research of analytical, modeling or experimental orientation; can solve and interpret complex cases that come up during this process.					X
7	Can communicate verbally and in writing in one foreign language (English) at the General Level B2 of the European Language Portfolio.					X
8	Can assume leadership in multi-disciplinary teams; can develop solutions in complex situations, and take responsibility.					X
9	Can systematically and openly communicate in national and international venues the proceedings and conclusions of the work he/she performs in Electrical and Electronics Engineering.					X
10	Respects social, scientific and ethical values in all professional activities performed during the collection, interpretation and announcement phases of data.					X
11	Is aware of new and emerging applications in Electrical and Electronics Engineering; investigates and learns them, whenever necessary.					X
12	Can identify the social and environmental aspects of Electrical and Electronics Engineering applications.					X

ECTS ALLOCATED BASED ON STUDENT WORKLOAD BY THE COURSE DESCRIPTION			
Activities	Quantity	Duration (Hour)	Total Workload (Hour)
Thesis study	14	107	1498
Total Work Load			1498
Total Work Load / 25 (h)			59.92
ECTS Credit of the Course			60